

B.Sc.(H),Part.-II
Practical Exam,2020
ELTA-IVB
Full Marks-25

Answer any five questions (5x5=25)

1. State and De Morgan's Theorem for 2- variables. Explain in brief, how can you verify the theorems in the laboratory.
2. What do you mean by a universal logic gate? How to use any one such gate to construct an AND, OR and NOT gates?
3. How may you construct OR, AND gate using discrete components (resistors, diodes/transistors) and verify the respective truth-tables.
4. Draw the static input characteristics of a p-n-p transistor in CB-configuration. How can you calculate the input impedance from it.
5. Draw the output characteristics of an n-p-n transistor in CE configuration. How can you calculate the current-gain from the characteristics.
6. Differentiate between the uses of BJT and UJT.
- 7.State a few applications of XOR gate.

Students should submit their answer copy to the following email id

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